

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Toshitada SAITO)
) Group Art Unit:
Serial No.: Not Yet Assigned)
) Examiner:
Filed: December 28, 2001)
)
For: SYSTEM AND METHOD FOR)
RECEIVING OFDM SIGNAL)
)

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

PRELIMINARY AMENDMENT

Prior to the examination of the above application, please amend this application as follows. A marked up version showing the changes made is attached.

IN THE CLAIMS:

8. (Amended) The system for receiving an OFDM signal according to claim 7,
- wherein, in response to said detected modulation system, said operational unit includes:
- a first shift circuit which sets first data close to a side of the most significant bit and sets "0" data close to a side of the least significant bit;
- a second shift circuit which sets second data close to the side of the most significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second shift circuits.

11. (Amended) The system for receiving an OFDM signal according to claim 10,

wherein, in response to said detected modulation system, said operational unit includes:

a first shift circuit which sets first data close to a side of the most significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second shift circuits.

14. (Amended) The system for receiving an OFDM signal according to claim 13,

wherein, in response to said detected modulation system, said operational unit includes:

a first shift circuit which sets first data close to a side of the most significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second shift circuits.

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17. (Amended) The system for receiving an OFDM signal according to claim 16,
wherein, in response to said detected modulation system, said operational unit includes:

a first shift circuit which sets first data close to a side of the most significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second shift circuits.

20. (Amended) The system for receiving an OFDM signal according to claim 19,

wherein, in response to said detected modulation system, said operational unit includes:

a first shift circuit which sets first data close to a side of the most significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second shift circuits.

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REMARKS

Applicant amends claims 8, 11, 14, 17 and 20, to improve grammar. No new matter has been introduced by these amendments.

An amended form of claim 8,11,14,17 and 20 is attached for the Examiner's convenience pursuant to rule 37 C.F.R. §1.21(c)(1)(ii).

If there is any fee due in connection with the filing of this Preliminary Amendment, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: December 28, 2001

By: 

Richard V. Burgujian
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Marked Up Version Showing The Changes Made

8. The system for receiving an OFDM signal according to [any one of claims]
claim 7,

wherein, in response to said detected modulation system, said operational
unit includes:

a first shift circuit which sets first data close to a side of the most
significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most
significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second
shift circuits.

11. The system for receiving an OFDM signal according to [any one of claims]
claim 10,

wherein, in response to said detected modulation system, said operational
unit includes:

a first shift circuit which sets first data close to a side of the most
significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most
significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second
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14. The system for receiving an OFDM signal according to [any one of claims]
claim 13,

wherein, in response to said detected modulation system, said operational
unit includes:

a first shift circuit which sets first data close to a side of the most
significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most
significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second
shift circuits.

17. The system for receiving an OFDM signal according to [any one of claims]
claim 16,

wherein, in response to said detected modulation system, said operational
unit includes:

a first shift circuit which sets first data close to a side of the most
significant bit and sets "0" data close to a side of the least significant bit;

a second shift circuit which sets second data close to the side of the most
significant bit and sets "0" data close to the side of the least significant bit; and

an operational circuit operating data outputted from said first and second
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20. The system for receiving an OFDM signal according to [any one of claims]
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a first shift circuit which sets first data close to a side of the most significant bit and sets "0" data close to a side of the least significant bit;

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